Amendments to the Claims:

1. (CURRENTLY AMENDED) A method to display a source image on a LCD panel without scaling comprising the steps of:

transferring an even image line to line buffers,

transferring the output of said line buffers to an input of LCD panel drivers of an upper half portion within the LCD panel.

skipping an LCD ~~Vsync pl~~ Vertical synchronization pulse at the end of a display within said even image lines.

transferring odd image lines to line buffers,

transferring the output of said line buffers to the input of the LCD panel drivers of a lower half portion within the LCD panel,

blanking the data of said odd image of said lower portion of the LCD screen.

2. (CURRENTLY AMENDED) The method of displaying a source image on an LCD panel without scaling of claim 1 further comprising the steps of:

moving a no-scaling image up or down on the LCD panel by shifting the timing of the vertical synchronization ~~Vsync~~ Vertical synchronization of the LCD panel to the left or right in the time domain.

3. (ORIGINAL) The method of displaying a source image on an LCD panel without scaling of claim 1 further comprising the steps of:

limiting the requirement for a frame buffer.

4. (ORIGINAL) A method of displaying a source image on an LCD panel without scaling and without an image buffer comprising the steps of:

skipping on vertical synchronization pulse every two normal vertical sync time periods,

displaying the even lines of the source image,

blanking the lower portion of the LCD image and,

shifting the vertical synchronization pulse in order to shift the LCD image to the center of the LCD displayable area.

5. (CURRENTLY AMENDED) An apparatus to display a source image on a LCD panel without scaling comprising:

a means for transferring an even image line to line buffers,

a means for transferring an output of said line buffers to an input of LCD panel drivers of an upper half portion within the LCD panel.

a means for skipping LCD ~~Vsync-p~~ Vertical synchronization pulse at the end of a display within said even image lines,

a means for transferring odd image lines to line buffers,

a means for transferring an output of said line buffers to an input of LCD panel drivers of a lower half portion within the LCD panel.

a means for blanking data of said odd image of said lower portion of the LCD screen.

6. (CURRENTLY AMENDED) The apparatus of claim 5 further comprising:

a means for moving a no-scaling image up or down on the   LCD panel

by shifting the timing of the ~~vertical~~ Vertical synchronization ~~Vsync~~ of the LCD panel to the

left or right in the time domain.

7. (ORIGINAL) The apparatus of claim 5 wherein there is no requirement for a frame

buffer.

8. (ORIGINAL) The apparatus of claim 5 wherein said means for transferring the even

image lines to line buffers further comprising:

a means for performing said transfer utilizing direct connections between the

output of the image buffer and the line buffers of the LCD panel.

9. (ORIGINAL) The apparatus of claim 5 where said means for transferring the odd

image lines to line buffers further comprising:

a means for performing said transfer utilizing direct connections between the

output of the image buffer and the line buffers of the LCD panel.

10. (ORIGINAL) The apparatus of claim 5 where said means for transferring the output

of said line buffers to the input of the LCD panel drivers of the upper half portion within

the LCD panel further comprising:

a means for performing said transfer utilizing direct connections between said

line buffers and said LCD panel drivers.

11. (CURRENTLY AMENDED) The apparatus of claim 5 where said means for skipping

the LCD ~~Vsync-pl~~ Vertical synchronization pulse at the end of a display within said even

image lines further comprising:

a means for performing said skipping utilizing a frequency divider to divide the

image source buffer ~~Vsync~~ Vertical synchronization by two.

12. (ORIGINAL) The apparatus of claim 5 where said means for blanking the data of

said odd image of said lower portion of the LCD screen further comprising:

a means for performing said blanking utilizing a logic circuitry which senses the

odd frame time domain.

13. (CURRENTLY AMENDED) The apparatus of claim 6 where said means for moving

the no-scaling image up or down on the LCD panel by shifting the timing of the ~~vertical~~

Vertical synchronization ~~signal, Vsync~~ of the LCD panel to the left or right in the time

domain further comprising:

a means for performing said moving of ~~Vsync~~ Vertical synchronization for the

LCD panel by utilizing a shift register to shift the ~~Vsync~~ Vertical synchronization the

required amount to the left or right in the time domain.

14. (ORIGINAL) An apparatus for displaying a source image on an LCD panel without

scaling and without an image buffer comprising:

a means for skipping on vertical synchronization pulse every two normal

vertical sync time periods,

a means for displaying the even lines of the source image,

a means for blanking the lower portion of the LCD image and,

a means for shifting the vertical synchronization pulse in order to shift the

LCD image to the center of the LCD displayable area.

15. (CURRENTLY AMENDED) A program retention device containing program

instruction code executable on at least one networked computing device for simulating a

model of an LCD panel without scaling, whereby said program performs the steps of:

transferring the even image line to line buffers,

transferring the output of said line buffers to the input of the LCD panel

drivers of the upper half portion within the LCD panel.

skipping the LCD ~~Vsync-pl~~ Vertical synchronization pulse at the end of a

display within said even image lines.

transferring the odd image lines to line buffers,

transferring the output of said line buffers to the input of the LCD

panel drivers of the lower half portion within the LCD panel.

blanking the data of said odd image of said lower portion of the LCD

screen.

16. (CURRENTLY AMENDED) The program retention device of claim 15, wherein said

program further performs the step of:

moving the no-scaling image up or down on the LCD panel by shifting the

timing of the ~~vertical~~ Vertical synchronization ~~Vsync~~ of the LCD panel to the left or right in

the time domain.

17. (ORIGINAL) The program retention device of claim 15, wherein said program

eliminates the requirement for a frame buffer.

18. (ORIGINAL) A program retention device for displaying a source image on an LCD

panel without scaling and without an image buffer, whereby said program performs the

steps of:

        skipping on vertical synchronization pulse every two normal vertical sync

time periods,

        displaying the even lines of the source image,

        blanking the lower portion of the LCD image and,

        shifting the vertical synchronization pulse in order to shift the LCD image to

the center of the LCD displayable area.